

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: **Hiroaki OHKUBO, et al.** Examiner: **Unassigned**
Serial No: **Unassigned** Art Unit: **Unassigned**
Filed: **Herewith** Docket: **17525**
For: **SEMICONDUCTOR INTEGRATED
CIRCUIT DRIVE** Dated: **March 9, 2004**

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. §§ 1.97 and 1.98, it is requested that the following references, which are also listed on the attached Form PTO-1449, be made of record in the above-identified case.

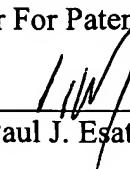
1. Japanese Patent Laid-Open Publication No. Hei 1-302849, December 6, 1989, together with English-language Abstract;
2. Japanese Patent Laid-Open Publication No. Hei 9-229778, dated September 5, 1997, together with English-language Abstract; and

CERTIFICATE OF MAILING BY EXPRESS MAIL

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I hereby certify that this correspondence is being deposited with the United States Postal Service Express Mail Post Office to Addressee service under 37 C.F.R. §1.10 on the date indicated above and is addressed to the Commissioner For Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Dated: March 9, 2004


Paul J. Esatto, Jr.

3. Japanese Patent Laid-Open Publication No. Hei 63-300523, dated December 7, 1988, together with English-language Abstract;

Applicants are submitting copies of the above-cited references. The relevance of the References 1 and 2 has been described in the specification.

Inasmuch as this Information Disclosure Statement is being submitted in accordance with the schedule set out in 37 C.F.R. § 1.97(b), no statement or fee is required.

Respectfully submitted,



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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.